

# **BC56PAST** series

# 80 V, 1 A NPN medium power transistors

Rev. 2 — 27 November 2024

**Product data sheet** 

### 1. General description

NPN medium power transistors in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with medium power capability and Side-Wettable Flanks (SWF).

**Table 1. Product overview** 

Type number	Package	PNP complement
BC56PAST	DFN2020D-3 (SOT1061D)	BC53PAST
BC56-10PAST		BC53-10PAST
BC56-16PAST		BC53-16PAST

### 2. Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- · Three current gain selections
- · Reduced Printed-Circuit Board (PCB) area requirements
- Leadless small SMD plastic package with solderable side pads
- · Exposed heat sink for excellent thermal and electrical conductivity
- Suitable for Automatic Optical Inspection (AOI) of solder point

# 3. Applications

- Linear voltage regulators
- MOSFET drivers
- High-side switches
- Power management
- Amplifiers
- · Bettery driven devices

### 4. Quick reference data

#### Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	80	V
I <sub>C</sub>	collector current		-	-	1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	2	Α



Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h <sub>FE</sub>	DC current gain						
	BC56PAST	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	250	
	BC56-10PAST		[1]	63	-	160	
	BC56-16PAST		[1]	100	-	250	

[1] pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

# 5. Pinning information

### Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	C
2	E	emitter		В
3	С	collector	Transparent top view	E sym021

# 6. Ordering information

### **Table 4. Ordering information**

Type number	Package					
	Name	Description	Version			
BC56PAST		plastic, leadless thermal enhanced ultra thin small outline	<u>SOT1061D</u>			
BC56-10PAST		package with side-wettable flanks (SWF); no leads; 3 terminals; 1.3 mm pitch; 2 mm x 2 mm x 0.65 mm body				
BC56-16PAST		terminais, 1.3 mm pitch, 2 mm x 2 mm x 0.05 mm body				

# 7. Marking

### Table 5. Marking

Type number	Marking code
BC56PAST	F7
BC56-10PAST	F6
BC56-16PAST	F5

# 8. Limiting values

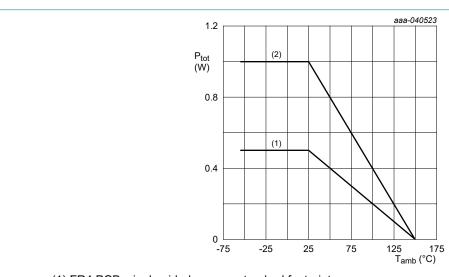
#### **Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	100	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	open collector		5	V
I <sub>C</sub>	collector current			-	1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	2	Α
I <sub>B</sub>	base current			-	0.2	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	0.3	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.5	W
			[2]	-	1	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



- (1) FR4 PCB; single-sided copper; standard footprint
- (2) FR4 PCB; single-sided copper; 1 cm<sup>2</sup>

Fig. 1. Power derating curves

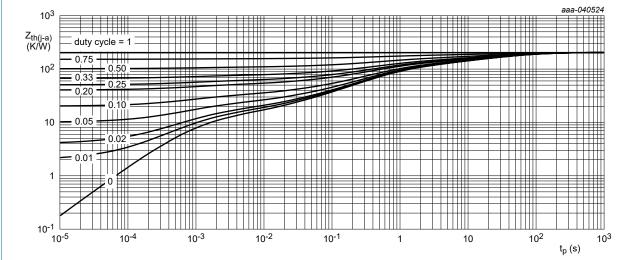
## 9. Thermal characteristics

#### **Table 7. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

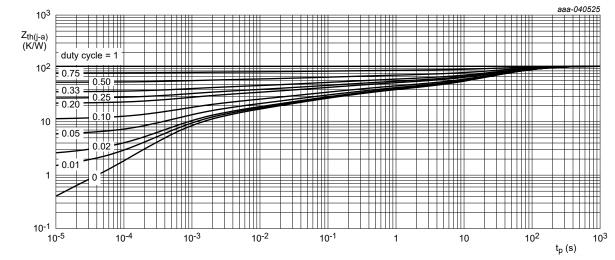
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	235	K/W
			[2]	-	-	124	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	15	K/W

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



FR4 PCB; single-sided copper; tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

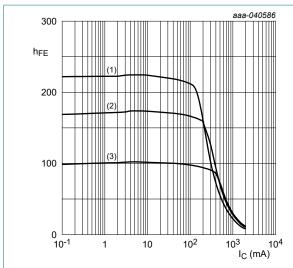
# 10. Characteristics

### **Table 8. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		100	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}$		80	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A		5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A		-	-	100	nA
h <sub>FE</sub>	DC current gain		'	'	'	'	
	BC56PAST	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
	BC56-10PAST	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	160	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
	BC56-16PAST	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	100	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	500	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	-	-	1	V
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	4.5	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz		100	-	-	MHz

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 



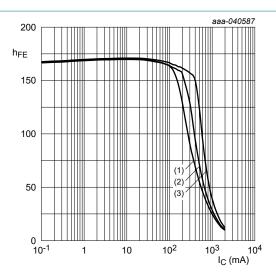
 $V_{CE} = 2 V$ 

(1)  $T_{amb} = 100 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = -55 \, ^{\circ}C$ 

Fig. 4. DC current gain as a function of collector current; typical values



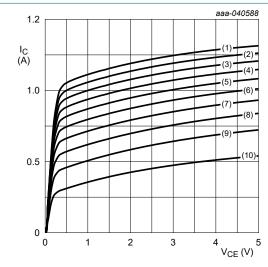
 $T_{amb}$  = 25 °C

 $(1) V_{CE} = 1 V$ 

(2)  $V_{CE} = 2 V$ 

(3)  $V_{CE} = 5 V$ 

Fig. 5. DC current gain as a function of collector current; typical values



 $T_{amb}$  = 25 °C

(1)  $I_B = 30.0 \text{ mA}$ 

(2)  $I_B = 27 \text{ mA}$ 

(3)  $I_B = 24 \text{ mA}$ 

 $(4) I_B = 24 \text{ mA}$ 

 $(5) I_B = 18 \text{ mA}$ 

(6)  $I_B = 15 \text{ mA}$ 

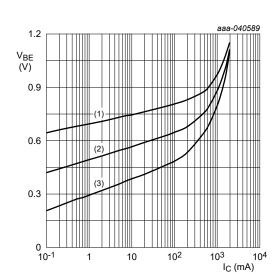
 $(7) I_B = 12 mA$ 

(8)  $I_B = 9 \text{ mA}$ 

(9)  $I_B = 6 \text{ mA}$ 

 $(10) I_B = 3 mA$ 

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



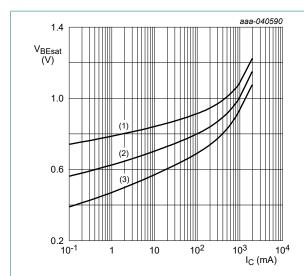
 $V_{CE} = 2 V$ 

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 100 \, ^{\circ}C$ 

Fig. 7. Base-emitter voltage as a function of collector current; typical values



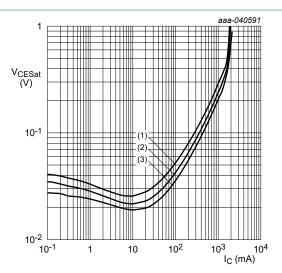
$$I_{\rm C}/I_{\rm B}=10$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



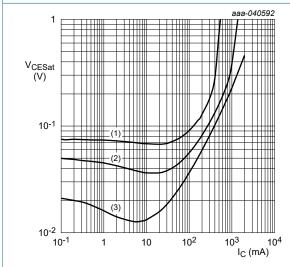
$$I_{\rm C}/I_{\rm B} = 10$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb} = 25 \, ^{\circ}C$$

(1) 
$$I_C/I_B = 50$$

(2) 
$$I_C/I_B = 20$$

(3) 
$$I_C/I_B = 5$$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

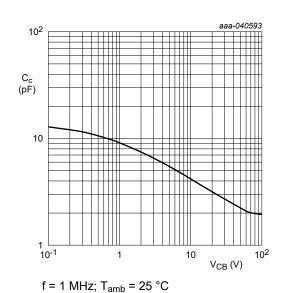


Fig. 11. Collector capacitance as a function of collectorbase voltage; typical values

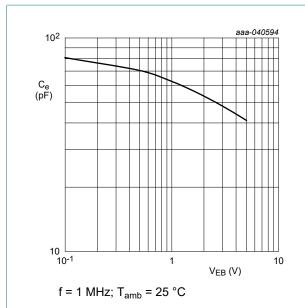


Fig. 12. Emitter capacitance as a function of emitterbase voltage; typical values

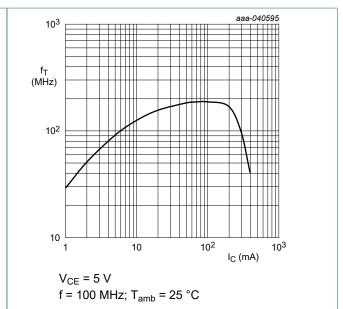
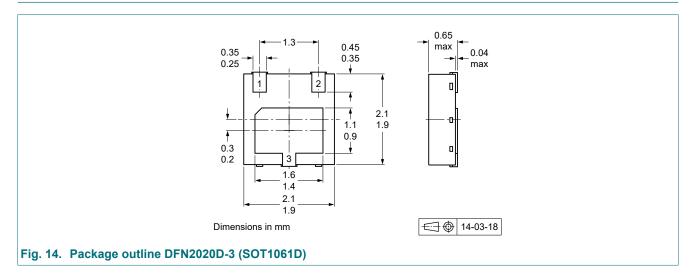
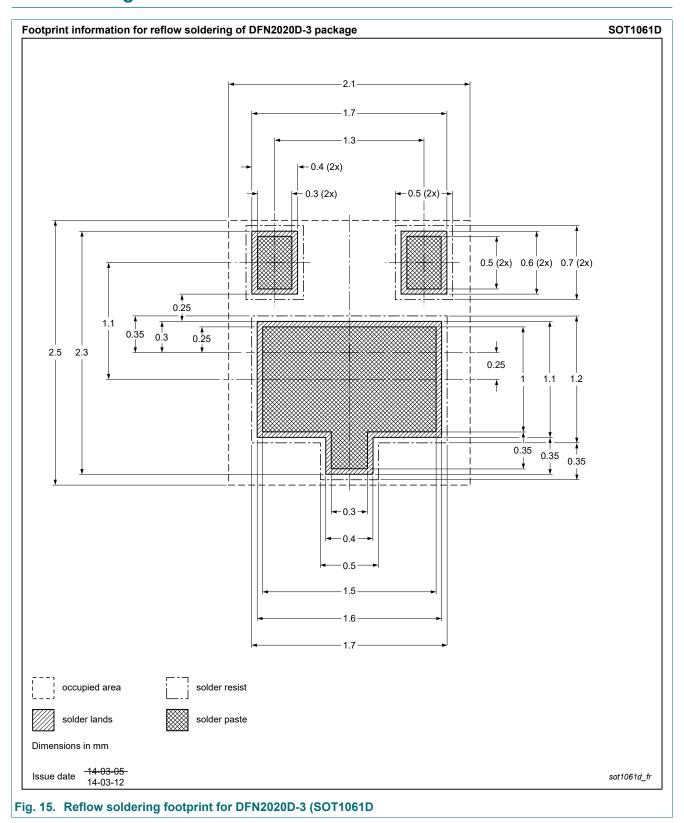


Fig. 13. Transition frequency as a function of collector current; typical values

# 11. Package outline



# 12. Soldering



# 13. Revision history

### Table 9. Revision history

Table 0. Revision motory							
Document ID	Release date	Data sheet status	Change notice	Supersedes			
BC56PAST_SER v.2	20241127	Product data sheet	-	BC56PAST_SER v.1			
Modifications:	General descrip	General description: Product overview corrected					
BC56PAST_SER v.1	20240823	Product data sheet	-	-			

### 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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